

**IN THE SPECIFICATION:**

Please replace the first full paragraph of specification page 1 with the following replacement paragraph:

— The present invention is related to the following co-pending and commonly assigned U.S. Patent Application Serial No. ~~(112056-0016)~~10/008,497 titled, *Concentrated Parity Technique for Handling Double Failures and Enabling Storage of More than One Parity Block per Stripe on a Storage Device of a Storage Array*, which was filed on even date herewith and which application is hereby incorporated by reference as though fully set forth herein. —